CC-DAY 2026

Programme & Technical Sessions

KEYNOTES / ENGINEERING / INNOVATION

TUESDAY, 17 NOVEMBER 2026

Trafo Baden | Switzerland

From 13:00 | Flying Dinner from 18:00

ccontrols.net/cc-day-2026

Programme | 14:00–15:00

Arrival, opening and first technical sessions

13:00–14:00	Arrival & Coffee · Doors Open
14:00–14:10	Welcome · CControls · Main Hall
14:10–14:25	AMZ Racing · World-Record-Innovation

PARALLEL TECHNICAL SESSIONS

14:30–15:00

14:30–15:00 Main Hall	Keysight	Scaling High-Speed Digital Receiver Validation: From PCIe and USB to the Next Generation of AI Interconnects, Part 1 Vitaly Morarenko
14:30–15:00 Room 2	RIGOL	Dynamic Characterization of SiC/GaN Power Devices in 800V DC Data Center Power Architectures Wang Fangfang (Angela) · Technical Marketing Manager
14:30–15:00 Room 3	ThinkSight	From Scattered Data to Intelligent Knowledge: Enterprise AI with RAG Speaker: Michael Ziegler
14:30–15:00 Room 4	Hagiwara	Industrial SSDs Beyond the Datasheet: Performance, Power Loss Protection and Data Retention
14:30–15:00 Room 5	Helbling	Refactor or Replace? A Practical Guide to Legacy Systems in the AI Era Phillipp Butschle

Programme | 15:00–16:45

Afternoon technical tracks and networking

PARALLEL TECHNICAL SESSIONS

15:00–15:30

15:00–15:30
Main Hall

Keysight
Scaling High-Speed Digital Receiver Validation: From PCIe and USB to the Next Generation of AI Interconnects, Part 2
Vitaly Morarenko

15:00–15:30
Room 2

Pico Technology
Click, Code or AI? Automating Measurements with PicoScope
Speaker: Patrick Gold

15:00–15:30
Room 3

Dassault Systèmes
How Virtual Twins are shaping the Next Industrial Revolution
Speaker: Arnauld Pallier

15:00–15:30
Room 4

Y.I.C. Technologies
Exploring EMC/EMI Issues with Real-Time Scanning Solutions
Yoram Shimon

15:00–15:30
Room 5

PI Electronics AG
Topic to follow

15:30–16:15 **Networking Break**

Programme | 16:15–16:45

Technical sessions across all five rooms

PARALLEL TECHNICAL SESSIONS

16:15–16:45

16:15–16:45
Main Hall

Keysight
From PNA-X to PNA-X Pro: The Evolution of High-Performance VNAs, Part 1
Sergio Dibello · Solution Engineer, Keysight Technologies

16:15–16:45
Room 2

Keysight
End-to-End NTN Test and Validation Solutions: From Lab Emulation to Field Measurements, Part 1
Zineb Boudalal · Keysight Technologies

16:15–16:45
Room 3

Dassault Systèmes
The Cyber Resilience Act Explained
Speaker: Dennis Haarmann

16:15–16:45
Room 4

Byte Lab
AI That Sees, Hears, and Smells - From Coffee Beans to Drones: Edge AI in the Real World
Real-world Edge AI, from sensing and traffic analysis to intelligent rail monitoring.
Matija Puškar · CEO, Byte Lab

16:15–16:45
Room 5

Agilita
Topic to follow

Programme | 16:45–21:00

Final sessions, keynote and evening programme

PARALLEL TECHNICAL SESSIONS

16:45–17:15

16:45–17:15
Main Hall

Keysight
From PNA-X to PNA-X Pro: The Evolution of High-Performance VNAs, Part 2
Sergio Dibello · Solution Engineer, Keysight Technologies

16:45–17:15
Room 2

Keysight
End-to-End NTN Test and Validation Solutions: From Lab Emulation to Field Measurements, Part 2
Zineb Boudalal · Keysight Technologies

16:45–17:15
Room 3

Razorleaf International
PLM Without the Big Bang: Start Smart, Scale with Confidence
A Practical Roadmap from SOLIDWORKS to 3DEXPERIENCE

16:45–17:15
Room 4

SIMUSERV
Topic to follow

16:45–17:15
Room 5

Zühlke
Giving AI Agents a Seat at the LabVIEW and TestStand Bench
Niko Naredi-Rainer

17:25–17:55 · KEYNOTE · MAIN HALL

Beyond Prompting

Thomas Haas | How AI Changes the Way We Work

18:00–21:00 · EVENING PROGRAMME

Flying Dinner & Networking

Connect with fellow attendees and technology partners.

Keysight | Premium Partner

Featured technical sessions

PREMIUM PARTNER SPOTLIGHT

Three technical tracks from Keysight Technologies

01 HIGH-SPEED DIGITAL — 14:30–15:30 · Main Hall

Scaling High-Speed Digital Receiver Validation: From PCIe and USB to the Next Generation of AI Interconnects

Explore practical validation approaches for high-speed receivers, from PCIe and USB to next-generation AI interconnects.

Vitaly Morarenko · Keysight Technologies

02 VNA TECHNOLOGY — 16:15–17:15 · Main Hall

From PNA-X to PNA-X Pro: The Evolution of High-Performance VNAs

Discover the PNA-X architecture and the advantages PNA-X Pro brings to advanced network analysis.

Sergio Dibello · Solution Engineer, Keysight Technologies

03 NTN VALIDATION — 16:15–17:15 · Room 2

End-to-End NTN Test and Validation Solutions: From Lab Emulation to Field Measurements

Follow non-terrestrial network validation from controlled lab emulation to real-world field measurements.

Zineb Boudalal · Keysight Technologies

Each Keysight technical track is divided into two 30-minute sessions.

Keynote & Networking

A closer look at the closing keynote

17:25–17:55 · MAIN HALL

Beyond Prompting

How AI Changes the Way We Work

Thomas Haas

ABOUT THE KEYNOTE

AI looks almighty. Some claim it makes teams twenty times faster, with better quality. Does that hold up? In daily work, we are still prompting around. Thomas Haas shows how to move from prompts to a real process, using habits engineers already have, so that teams go beyond what they thought possible.

FIVE KEY IDEAS

01 Almighty AI?
AI cracks problems that have sat on the desk for years. Or so it seems.

02 Reality Check: Prompting Around
Daily work often means rephrasing prompts and restarting from scratch.

03 From Prompts to Process
Clear rules and verified results make AI useful in real workflows.

04 Engineering the Way We Work
Testing and iteration help integrate AI into everyday engineering.

05 Shifting Gears
Teams can accelerate outcomes when leaders make room to begin.

18:00–21:00 · TRAFO BADEN

Flying Dinner & Networking

Continue the conversation with attendees, speakers and partners.